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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Steven K. Hsu et al.
Title: VOLTAGE-LEVEL CONVERTER

Docket No.: 884.453US1
Filed: June 29, 2001
Examiner: Anh Q. Tran

Serial No.: 09/896,523
Due Date: December 10, 2002
Group Art Unit: 2819

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Commissioner for Patents
Washington, D.C. 20231

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We are transmitting herewith the following attached items (as indicated with an "X"):

- ☒ A return postcard.
- ☒ An Amendment and Response Under 37 CFR 1.116 (3 Pages).
- ☒ Clean Version of Pending Claims (2 Pages).

Please consider this a **PETITION FOR EXTENSION OF TIME** for sufficient number of months to enter these papers and please charge any additional required fees or credit overpayment to Deposit Account No. 19-0743.

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.
P.O. Box 2938, Minneapolis, MN 55402 (612-373-6900)

By: Danny J. Padys
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CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: BOX AF, Commissioner for Patents, Washington, D.C. 20231, on this 12 day of November, 2002.

Danny J. Padys
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Danny J. Padys
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Customer Number 21186

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EXPEDITED PROCEDURE - EXAMINING GROUP 2819

S/N 09/896,523

PATENT

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Applicant:	Steven K. Hsu et al.	Examiner:	Anh Q. Tran
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AMENDMENT & RESPONSE UNDER 37 C.F.R. § 1.116

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In response to the final office action mailed September 10, 2002, please amend the application as follows.

IN THE CLAIMS

Please substitute the claim set in the appendix entitled Clean Version of Pending Claims for the previously pending claim set. The substitute claim set is intended to reflect amendment of previously pending claims 1 and 32. The specific amendments to individual claims are detailed in the following marked up set of claims.

Please amend claims 1 and 32.

1. (Twice.Amended) A voltage-level converter comprising:
a static voltage-level converter including an inverter coupled to no more than two transistors in the static voltage-level converter each of the no more than two transistors directly coupled to a voltage level; and
a split-level output circuit coupled to the static voltage-level converter.
32. (Amended) The voltage level converter of claim [32] 2, wherein the second transistor of the first pair of transistors and the second transistor of the second pair of transistors are insulated gate field-effect transistors.